

RESUME



CONTACT DETAILS

P.ANBARASI

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CAREER OBJECTIVE

I am a hardworking focused and reliable person who wants to fully exploit my abilities and experience and contribute to the productivity and achievement of the organization.

ACADEMIC DETAILS

QUALIFICATION	INSTITUTION	UNIVERSITY/ BOARD	YEAR OF PASSING	AGGREGATE
M.E(VLSI_DESIGN)	PPG Institute of Technology,Coimbatore	Anna University	2012_2014	76.5
B.E (ECE)	Coimbatore Institute of Engineering and Technology	Anna University	2009_2012	70.7
DIPLOMA	Nanjiah Lingammal Polytechnic College,Mettupalayam	Directorate Of Technical Education	2006_2009	88.25
SSLC	Laymens Evangelical Fellowship Matriculation School,Mettupalayam	Matriculation	2006	75

EXPERIENCE

- Worked as an assistant prof in Sri Krishna Engineering College from 2014 to 2015.
- Now currently working in Prince Shri Venkateshwara Padmavathy Engineering college as a assistant professor from 1/06/2024 to till date.

MAIN PROJECT

- Voltage regulator dc_dc converter based on filter for low power analysis.
- Removal of image and video artifacts using a non_linear decision based algorithm.

MINI PROJECT

- Presented a project on “CALL BELL GUEST COUNTER”.

SKILL ACHIEVEMENT

- Presented a paper on “DSP based on_line UPS”at Jayalakshmi Institute of Technology,Dharmapuri .
- Presented a paper on “Brain Controlled Car”at CIET ,Coimbatore.
- Attended “In_plant training”at BSNL, Coimbatore.
- Attended 3 national conferences.
- Published a paper in international organization of scientific research journals

FDP ATTENDED

- Participated and completed ATAL FDP on AI and Quantum Computing: Glimpsing the Near Future at Sri Sairam Engineering College from 20/01/2025 to 25/01/2025.
- Participated and completed ATAL FDP on Reconfigurable Intelligent Surfaces for 6G Wireless Communication: Fundamentals to Future Research Directions at National Institute of Technology, Tiruchirappalli from 23/12/2024 to 28/12/2024.
- Participated and completed ATAL FDP on High performance computing for scientific research and simulations at Erode Sengunthar Engineering college from 16/12/2024 to 21/12/2024.
- Participated and completed ATAL FDP on Exploring Advanced Data Science Tools Techniques and Trends Anand Institute of higher technology from 09/12/2024 to 14/12/2024.

AREA OF INTEREST

- Digital Electronics

SUBJECT HANDLED

- Microprocessor
- Linear Integrated Circuits
- Communication Engineering
- Wireless Communication

MANAGEMENT

- Managing students by providing guidance and feedback to help them strengthen their skills and knowledge base.
- Excellent time management skills to ensure targets are met and plans completed efficiently.
- Involved in devising future plans to remain student focused.
- Able to contribute to the intellectual life and debate of an academic organization by conducting research which will enhance its reputation as a research_led teaching

institution.

- Willing to take responsibility for the quality of teaching delivered and to make necessary changes to improve quality and maintain standards.
- Able to organize conferences and establish contacts with the wider academic community.

MEMBERSHIPS

- Life Member in ISTE(LM 142967)

KEY COMPETENCIES AND SKILLS

- Mentoring
- Target Setting
- Training
- IT skills
- Research
- Planning

PERSONAL DETAILS

Date of Birth : 05-04-1990
Father's Name : Mr.Palaniraj V
Mother's Name : Mrs.Bharathi P
Sex : Female
Nationality : Indian
Languages Known : English, Tamil, Kannada

DECLARATION

I consider myself familiar with electronics aspects and I, hereby declare that the information furnished above is true to the best of my knowledge.

Place :
Date :

Signature
P.ANBARASI