SRIVIDHYA S

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Career objective

To work in learning and challenging environment, utilizing my skills and knowledge to be the best of my abilities and contribute positively to my personal growth as well as growth of organization.

Educational Qualification

COURSE	INSTITUTION/UNIVERSITY	YEAR	PERCENTAGE/ CGPA SECURED
Ph.D	Pondicherry Engineering college, (Govt. of Puducherry Institution) Puducherry.	2021	-
M.Tech (Electronics And Communication Engineering)	Pondicherry Engineering college, (Govt. of Puducherry Institution) Puducherry.	2015	8.81 (cgpa)
B. Tech (Electronics And Communication Engineering)	Perunthalaivar Kamarajar Institute of Engineering And Technology, (Govt. of Puducherry Institution) Karaikal.	2013	8.39 (cgpa)
HSC	Nirmala Ranee Girls Higher Secondary School, Karaikal.	2009	92%
SSLC	Nirmala Ranee Girls Higher Secondary School, Karaikal.	2007	82.8%

Work Experience and Position Held

- Assistant Professor, Department of Electronics and Communication Engineering at Prince Shri Venkateshwara Padmavathy Engineering College, Chennai (From 09.10.23 to Present)
- Teaching Assistant, Department of Electronics Engineering at Sardar Vallabhai National Institute of Technology, NIT Surat (From 18.11.22 to 20.01.23)
- Assistant Professor (on contract basis), Department of Electronics and Communication Engineering at Indian Institute of Information and Technology, IIIT Surat (From 08.08.22 to 17.11.22)
- Teaching Assistant, Department of Electronics Engineering at Sardar Vallabhai National Institute of Technology, NIT Surat (From 03.08.21 to 30.06.22)
- Half Time Teaching Assistantship, Department of Electronics and Communication Engineering atPondicherry Engineering college, Puducherry (From 03.08.2016 to 01.05.2018)

Research Interest

- Cryptography
- Visual cryptography
- Secret sharing schemes
- Machine Learning

Academic Projects

Doctorate of Philosophy

"*STUDIES TO IMPROVE QUALITY OF PROGRESSIVE AND MULTIPLE VISUAL CRYPTOGRAPHY SCHEMES USING MEANINGFUL SHARES*" at Pondicherry Engineering College (PEC)**,** Puducherry.

Post Graduate

"*Adaptive Halftoned Extended Visual Cryptography Scheme for enhancing the quality of host image***"** at Pondicherry Engineering College (PEC)**,** Puducherry.

Research Publications

1. S. Sridhar, R. Sathishkumar, and Gnanou Florence Sudha, "Adaptive halftoned visual cryptography with improved quality and security," **Multimedia Tools and Applications**, **Springer**, vol. 76, no. 1, pp. 815–834, 2015.

2. S. Srividhya, R. Sathishkumar, and G. F. Sudha, "Implementation of TiOISSS with meaningful shadows and with an additional authentication image," **Journal of Visual Communication and Image Representation**, **Elsevier**, vol. 38, pp. 284–296, 2016.

3. Srividhya Sridhar and Gnanou Florence Sudha, "Circular meaningful shares based (k, n) two in one image secret sharing scheme for multiple secret images", **Multimedia Tools and Application**, **Springer**, vol. 77, pp. 28601–28632, 2018.

4. Srividhya Sridhar and Gnanou Florence Sudha, "Quality improved *(k, n)* Priority Based Progressive Visual Secret Sharing",**Multimedia Tools and Application, Springer,** vol. 79, pp. 11459–11486, 2020.

5. S. Sridhar and Gnanou Florence Sudha, "Two in One Image Secret Sharing Scheme (TiOISSS) for extended progressive visual cryptography using simple modular arithmetic operations," **Journal of Visual Communication and Image Representation**, **Elsevier,** vol. 74, pp. 102996, 2021.

6. S. Srividhya, J. Jayasree, and G. F. Sudha, "Error Diffusion with Varying Threshold Halftoning for Enhancing Contrast of Color Images", **Innovations in Computer Science and Engineering (ICICSE 2017), Lecture Notes in Networks and Systems**, vol. 32, pp. 289–298, **Springer**, Singapore, 2019.

7. Srividhya Sridhar & Gnanou Florence Sudha, "Improving quality of Friendly Progressive Visual Secret Sharing using Essential Shares", **IEEE International Conference on Communication and Signal Processing (ICCSP 20)**, April 2020.

8. C. S. Kumar, V. A. Kandaswamy, P. S. B. Murugan, S. S.Srividhya, P. Arivazhagi & R. Lathamanju, "Empowering Agriculture: Classification of Diseases Affecting the Leaves and Spikes of Wheat Crop using Hybrid Deep Learning Methodology", **IEEE 8th International Conference on Smart Structures and Systems (ICSSS 2022)**, pp. 1–6, 2023.

9. R. Yadav, M. Kasim, S. Srividhya, Y. Kumar, T. Mohanraj & S. P. V. S. Rao, "The Techniques used for Sustainable Communication System FR 6-G type of N/W", **IEEE 4th International Conference on Advance Computing and Innovative Technologies in Engineering (ICACITE 2024),** pp. 79–84, April 2024.

10. S. Srividhya, R. Jain, K. Preetham, V. A. Meshram, K. Yuvaraj & M. Singh, "Analyzing the efficiency of Cross-Layer design for Location-Aware ad hoc networks", **IEEE 13th International Conference on Computing Communication and Networking Technologies (ICCCNT 2022)**, pp. 1–5, 2024.

11. S. Nivarthi, D. Wankhede, D. A. N., W. Goyal, S. S. & D. Aggarwal, "A Dual-Stage Deep Learning Model for Cyber Data Classification and Analysis", I**EEE Global Conference on Communications and Information Technologies (GCCIT 2024)**, pp. 1–7, 2024.

12. P. N. Mahalle, V. A. Kumar, A. Kataria, S. Srividhya, S. Dev & H. Malathi, "A Blockchain-Based Secure Communication Framework For IOMT With Improved Security Via Hybrid Metaheuristic Models", **IEEE International Conference on Recent Advances in Science and Engineering Technology (ICRASET 2024),** pp. 1–5, 2024.

13. A. Dev, S. S., S. Gupta, A. Rajiv, S. M. Katikar & S. R. Prasath, "Integrating Multi-Criteria Decision-Making for Improved Congestion Control in Wireless Sensor Networks", **IEEE 4th International Conference on ICT in Business Industry & Government (ICTBIG 2024),** pp. 1–6, 2024.

14. A. Vadukanathan, G. Duraikannu, V. Jaganathan, Srividhya Sridhar, and G. Suyambrakasam, "Enhanced Bank Locker security system utilizing RFID for Dual-Layer Protection," **IEEE 5th International Conference on Smart Electronics and Communication (ICOSEC)**, pp. 283–288, Sep. 2024.

Achievements and Extra-Curricular Activities

- Received a **project grant of ₹1 lakh** under the Capacity Building initiative of Unnat Bharat Abhiyan (UBA) for community development and institutional strengthening.
- **Topper in NPTEL** online course on Introduction to Machine Learning (Tamil)
- Served as a **Reviewer in IEEE transaction, Springer and IET journals**
- Certificate of Appreciation for significant contribution in **Organizing** the TEQIP-III Sponsored **International Conference** on "Advances in Computational Intelligence in Communication" (CIC 2016)
- Secured **Third Rank** in the M.Tech University Examination held on 2013-2014 at PEC, Puducherry
- Secured **First Rank** in CENTAC Entrance Examination held on 2013 at PEC, Puducherry

Training and Certification

FDP attended

- ✓ AICTE Training And Learning (ATAL) Academy sponsored "**Quantum Connection: Exploring interdisciplinary Applications and insights**"at Prince Shri Venkateshwara Padmavathy Engineering College

- ✓ AICTE Training And Learning (ATAL) Academy sponsored "**Emerging Trends and Technologies in Renewable Energy for Sustainable Development**" at Prince Shri Venkateshwara Padmavathy Engineering College

Short-Term course

- ✓ AICTE-QIP sponsored Short Term Course on *"Internet of Everything (IOE): Gaining Insights into an Emerging Digital Drive"*
- ✓ AICTE-QIP sponsored Short Term Course on "**Recent Trends and Developments in Broadband Technologies".**
- ✓ AICTE-ATAL sponsored Faculty Development Program on **"Quantum Connections: Exploring Interdisciplinary Applications And Insights"**

Workshops Attended

- ✓ Two days National level Workshop on "Emerging Directions in Image Processing and Understanding"
- ✓ Two days Workshop on "Numerical Computation Techniques using MATLAB"
- ✓ One day Workshop on "Digital Signal Processing-Demystified"
- ✓ One day Workshop on "Security Issues in Cloud Computing

Industrial Exposure

- ✓ Undergone In plant Training on "**FM Broadcast Equipments**"
- ✓ Undergone In plant Training on "**Fundamentals of Telecom**"

Certification course

- **NPTEL** online **Silver** certified course on "Introduction to Machine Learning (Tamil)"

Curricular Activities

Papers Presented

- ➢ Paper entitled "Error Diffusion with Varying Threshold Halftoning for Enhancing Contrast of Color Images" have been presented at Guru Nanak Institute of Technology through Skype mode.
- ➢ Paper entitled "Improving quality of Friendly Progressive Visual Secret Sharing using Essential Shares" have been presented at Adhiparasakthi Engineering College, Tamilnadu.

Responsibility Held in National Institute of Technology, Surat (SVNIT)

- ▪ Lab handling
- ▪ Teaching assistantship
- ▪ NBA and NAAC Accreditation works
- ▪ Organizing committee member in FDP

Subject Handled

- ▪ Digital Communication
- ▪ Mobile Communication
- ▪ Digital Integrated circuit
- ▪ Linear integrated circuits
- ▪ Digital Signal Processing
- ▪ Digital Electronics and Logic Design
- ▪ Electronic system design Lab
- ▪ Digital Communication Lab
- ▪ Digital Signal Processing Lab
- ▪ Digital Electronics and Logic Design Lab
- ▪ Speech Processing
- ▪ Wireless and Optical Communication

Personal Profile

Name	: Srividhya S
Father's Name	: Sridhar K
Mother's Name	: Jamuna K
Date of Birth	: 22.07.1992
Languages known	: Tamil, English
Hobbies	: Reading Books