

RESUME



VALARMATHY.A.S

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Educational Qualifications

- Pursuing PhD in Power Electronics , Department of SELECT, VIT University, Chennai.
- M.E. (Power Electronics & Industrial drives); Sathyabama University, CGPA: **8.91**, Secured **5th university rank holder**.
- M.B.A (Human Resource) from Madurai Kamaraj University,**70%**.
- B.E (Electrical and Electronics Engineering), I.R.T.T, Erode, Bharathiyar University with **79.5%**.

Work Experience

Academic:

- Presently Working as an Assistant Professor ,Department of Electrical and Electronics Engineering, Prince Shri Venkateshwara Padmavathy Engineering College , from December 2005 to till date.

Industry:

- Assistant Engineer (HR) from 2002 to 2005 at KYUNGSHIN INDUSTRIAL MOTHERSON LIMITED, Chennai.

Administrative Experience

- Course Coordinator for Value added course organized by Electrical and Electronics Department, Prince Shri Venkateshwara Padmavathy Engineering college.
- Internal Auditor for NAAC Accreditation, Prince Shri Venkateshwara Padmavathy Engineering college
- Exam cell member for internal Assessment Exams , Prince Shri Venkateshwara

Padmavathy Engineering college

- Active Member in Club activities (Women Empowerment Cell), Prince Shri Venkateshwara Padmavathy Engineering college
- IQAC member for NAAC , Prince Shri Venkateshwara Padmavathy Engineering college

Other Experience

- Experience as Examiner for Paper valuation for B.E.Courses.
- Experience as External Examiner for conducting the practical examinations for B.E courses
- Anna University Representative for university exams.
- Flying Squad Member for University Examinations.
- Project guide for UG graduates.
- Internal Quality Auditor –ISO /TS16949:2002.
- Participated in fourth annual convention of ISTE Student Chapters & National seminar

Short Term course Attended

- On “*Electromagnetic Theory*” FDP organized by the Dhanalakshmi College of Engineering (24th June to 26th June 2013)
- On “*Quality Assurance*” organized by Tata Consultancy Services (TCS), Siruseri, Chennai (1st March 2016).
- NPTEL Courses on Electrical Circuits and Electromagnetic Theory .

Key note Delivered

- Faculty Development Programme on “Electrical Machines II” at Valliammai Engineering College, SRM Campus, Chennai.

Publications

1. A.S.Valarmathy & R.Meenadevi, “*Implementation of TI-SEPIC converter for optimal utilization of PV power*” in 6th national Conference on Renewable Energy Sources, S.A.Engineering College on 1st February 2013.
2. A.S.Valarmathy & R.Meenadevi , “*Implementation of TI-SEPIC converter for optimal utilization of PV power- Closed loop*” in National Conference on Recent trends in Electrical and Communication Engineering, S.M.K Fomra Institute of Technology on February 8th and 9th 2013.
3. A.S.Valarmathy, “*Voltage Sag Mitigation using LC and DVR Technique*” in International Journal for Research & Development in Technology, Vol. 8, Issue 1, ISSN : 2349-3585,2017.
4. A.S.Valarmathy&G.Indira ,”*An efficient SMPS for DC motor*” in International Journal of Advance Research and Innovative Ideas in Education, Vol. 3, Issue 4, pp. 931-938, ISSN

- : 2396-4396,2017.
5. A.S.Valarmathy," *Implementation of Ti-Sepic Converter for Optimal Utilization of PV Power*" in International Journal of Trend in Scientific Research and Development, Vol. 1, Issue 5, ISSN: 2456-6470,2017.
 6. M.Yogeshwaran² and A.S.Valarmathy³ C.Akshay¹" Design and implementation of high gain converter using offshore WECS" in IJARIE-ISSN(O)-2395-4396,Vol 4, Issue 2, :2006-2011,2018.
 7. Vivekanandan S ² and Valaramathy A.S Saravanan VI" Interleaved converter with coupled inductor and dickson charge pump based voltage multiplier" in IJIRT, Vol 4,626-628,2018
 8. A.S.Valarmathy&P.Chandrakala,"*Ti-Sepic Converter with MPPT Technique for Optimal Utilization of PV Power*" International Journal for Science and Advance Research In Technology, Vol. 3, Issue 8, ISSN: 2395-1052,2017.
 9. A.S.Valarmathy & G.Indira" *Enhancement of Voltage Gain and Reduction in THD by Third Order One Sixth Harmonic Injection Techniques*" International Journal of Advance Research and Innovative Ideas in Education, Vol. 3, Issue 4, pp. 2706-2719, ISSN : 2396-4396,2017.
 10. G. Indira, A. S. Valarmathy, P. Chandrakala, S. Hemalatha, G. Kalapriyadarshini; *Development of an efficient inverter for self powered sand sieving machine. AIP Conf. Proc. 19 May 2022; 2393 (1): 020144. <https://doi.org/10.1063/5.0074347>*
 11. R. Harish, G. Indira, G. Kalapriyadarshini, A. S. Valarmathy, P. Chandrakala; *Analysis and implementation of DC power and appliances in conjunction with solar for large independent units. AIP Conf. Proc. 19 May 2022; 2393 (1): 020145. <https://doi.org/10.1063/5.0079651>*
 12. M. J. Murali, R. Harish, A. S. Valarmathy, G. Indira, S. Hemalatha; *ANFIS based MPPT for grid PV generation system using QZSI. AIP Conf. Proc. 19 May 2022; 2393 (1): 020147. <https://doi.org/10.1063/5.0074360>.*
 13. Valarmathy AS, M. K. Mokashi, S. K. Jakhar, V. Singh, A. KS and A. Kaur, "Harnessing AI to Achieve the Best Results from Data Science," 2023 3rd International Conference on Smart Generation Computing, Communication and Networking (SMART GENCON), Bangalore, India, 2023, pp. 1-7, doi: 10.1109/SMARTGENCON60755.2023.10442694.
 14. D. D. Winster Praveenraj, M. I. Habelalmateen, A. Shrivastava, A. Kaur, A. S. Valarmathy and C. P. Patnaik, "Behavioral Time Management Analysis: Clustering Productivity Patterns using K-Means," 2024 IEEE International Conference on Communication, Computing and Signal Processing (IICCCS), ASANSOL, India, 2024, pp. 1-6, doi: 10.1109/IICCCS61609.2024.10763887.

15. A. S. Valarmathy, P. Chandrakala, S. Hemalatha, G. Kalapriyadarshini, M. J. Murali; *Sepic with magnetic coupling for high voltage applications. AIP Conf. Proc. 19 May 2022; 2393 (1): 020148. <https://doi.org/10.1063/5.0074353>*
16. S. Hemalatha, G. Kalapriyadarshini, M. J. Murali, R. Harish, A. S. Valarmathy; *A comparative performance analysis and luminous characteristics of different commercial LED lighting systems. AIP Conf. Proc. 19 May 2022; 2393 (1): 020143.*
17. A.S. Valarmathy and Prabhakar Mahalingam, “High gain interleaved DC-DC converter with ripple-free input current and low device stress,” *International Journal of Electronics*. 1–26, 2024, DOI:10.1080/00207217.2024.2312569. **(SCI, Impact Factor – 1.3).**
18. A.S. Valarmathy and M. Prabhakar, “Non-isolated high gain DC–DC converter with ripple-free source current,” *Scientific Reports*, vol. 14, no. 1, 973, 2024, DOI:[10.1038/s41598-024-51584-9](https://doi.org/10.1038/s41598-024-51584-9). **(SCI, Impact Factor – 4.6).**
19. A.S. Valarmathy and M. Prabhakar, “High gain interleaved boost-derived DC-DC converters – A review on structural variations, gain extension mechanisms and applications,” *e-Prime - Advances in Electrical Engineering, Electronics and Energy*, vol. 8, 100618, 2024. **(Scopus indexed).**
20. Valarmathy, A. S., Prabhakar, M., & Varesi, K, “Non-isolated high gain DC-DC converter for PV-fed DC microgrid,” *International Journal of Sustainable Engineering*, vol. 17, no. 1, 32-50, 2024, DOI: 10.1080/19397038.2024.2420615. **(Scopus indexed, Impact Factor-3.6).**
21. A.S. Valarmathy and M. Prabhakar, “Coupled Inductor based High Gain DC-DC converter for renewable energy application,” 2nd International Conference on Power and Embedded Drive Control (ICPEDC), Chennai, India, 516-521, 2019.
22. A.S. Valarmathy and M. Prabhakar, “Comparison of coupled-inductor based interleaved high gain DC-DC converter topologies,” 7th International Conference on Electrical Energy Systems (ICEES), Chennai, India, 364-369, 2021.

Personal Information

Father’s Name : L.Anandasubramanian
 Date of Birth : 25th December 1979
 Marital Status : Married

(A.S.VALARMATHY)